

Docket No.: 67161-121

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Koji NII	:	Confirmation Number:
	:	
Serial No.:	:	Group Art Unit:
	:	
Filed: October 21, 2003	:	Examiner: Unknown
	:	
For:		SEMICONDUCTOR MEMORY DEVICE CAPABLE OF CONTROLLING POTENTIAL LEVEL OF POWER SUPPLY LINE AND/OR GROUND LINE

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

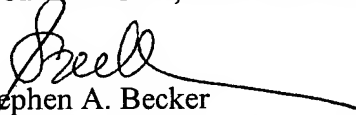
The relevance of the references listed on attached Form 1449 is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 67161-121		SERIAL NO.	
(PTO-1449)				APPLICANT Koji Nii			
				FILING DATE October 21, 2003		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ₂ (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US	5,726,562	03/10/1998	Mizuno		(Counterpart of JP 9-73784)
		US					
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number 4 - Kind Codes (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Figures Appear
		JP 9-73784		03/18/1997	NEC CORP		(Japan w/English Abstract)
		JP P2002-288984A		10/04/2002	ENOMOTO TADAYOSHI		(Japan w/English Abstract)
		JP 8-161890		06/21/1996	FUJITSU LTD		(Japan w/English Abstract)
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		"Dynamically Controllable DC Level Converter (DCLC) Technique to Reduce Power Dissipation, and Application to High-Speed, Low-Power Circuits", Yoshinori Oka et al., Technical Report of IEICE. SDM2001-122, ICD2001-45, August, 2001, pp. 69-76					
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.